

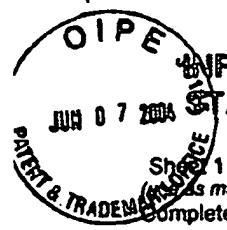
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Application No.	09/8015564	Prepared by	M RUSTA	Tracking Number	06803538
Examiner-GAU	MYERS-2112	Date	10-01-04	Week Date	08-30-04
		No. of queries	1	APW-E	

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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

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Sheet 1 of 1
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Application Number: 08/845,504
Filing Date: 04-30-2001
First Named Inventor: Kenneth Kotlowski
Group Art Unit: 2112
Examiner Name: Paul R. Myers
Attorney Docket Number: 1458-P04919

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. 1	U.S. Patent Document Number	Kind Code 2 (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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A11	JP	410285011	A	Aihara	10-23-1998	Abstract only	<input checked="" type="checkbox"/>
A12	DE	19949144	C1	Goetz	01-01-2001	Abstract only	<input checked="" type="checkbox"/>
A13							<input type="checkbox"/>

PUBLICATIONS

Examiner Initials *	Cite No. 1	Title of Publication	Date of Publication of Cited Document MM-DD-YYYY
	A14	Calder, B and Grunwald, D., "Fast and Accurate Instruction Fetch and Branch Prediction," Computer Architecture, Proceedings the 2nd Annual International Symposium on, 1 pp.	4/18-21/1994
	A15	John P. Uyemura, "CMOS Logic Circuit Design," Kluwer Academic Publishers, Boston, MA; ISBN 0-7923-8452-0, pp. 144 & 145	2001
	A16	Wee-Seng Soh and Kim, H.S., "Dynamic bandwidth reservation in hierarchical wireless ATM networks using GPS-based prediction," IEEE VTS 50th Vehicular Technology Conference, Amsterdam, Netherlands, on pp. 428-432 Vol. 1, [online] Retrieved from IEEE Xplore, Abstract, 2 pages	9-19-1999
	A17	Polze, A. et al., "Predictable Network Computing," Proceedings of the 17th International Conference on Distributed Computing Systems, Baltimore, MD, on pp. 423-431, [online] Retrieved from IEEE Xplore, Abstract, 2 pages	5-27-1997

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Title of Invention	SPLIT TRANSACTIONAL UNIDIRECTIONAL BUS ARCHITECTURE AND METHOD OF OPERATION						
Application Number:	09/845504						
Confirmation Number:	2092						
First Named Applicant:	Kenneth Kotlowski						
Attorney Docket Number:	1458-P04919						
Art Unit:	2112						
Examiner:	Paul R. Myers						
Search string:	(4969120 or 5007011 or 5412786 or 5539739 or 5581729 or 5623644 or 5634043 or 5717343 or 5740346 or 5859550 or 5933610 or 5991824 or 5248906 or 5528172 or 5996037 or 6145039 or 6185629 or 6205524 or 6256693 or 6292025 or 6335935 or 6415424 or 6504817 or 6578098 or 6252865 or 6333654 or 20020120878 or 20020110120).pn.						
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US Patent Documents							
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Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4969120	1990-11-06	Azevedo et al.			
	2	5007011	1991-04-09	Murayama			
	3	5412786	1995-05-02	Kusano			
	4	5539739	1996-07-23	Dike et al.			
	5	5581729	1996-12-03	Nishtala et al.			
	6	5623644	1997-04-22	Self et al.			
	7	5634043	1997-05-27	Self et al.			
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	15	5996037	1999-11-30	Emnett	
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	19	6256693	2001-07-03	Platko	B1
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	25	6252865	2001-06-26	Walton et al.	B1
	26	6333654	2001-12-25	Harris et al.	B1

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020120878	2002-08-29	Lapidus	A1		
	2	20020110120	2002-08-15	Hagglund et al.	A1		

Signature

Examiner Name	Date

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